

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:)	
)	Group Art Unit: 2133
William F. Beausoleil)	
)	Examiner: Thomas H. Stevens
Application No.: 09/655,595)	
)	Confirmation: 9321
Filed: September 6, 2000)	
)	
For: High Speed Software Driven Emulator)	
Comprised of a Plurality of Emulation)	
Processors with Improved Board-to-Board)	
Interconnection Cable Length Identification)	
System)	

TRANSMITTAL LETTER

Commissioner For Patents
P.O. Box 1450
Alexandria VA 22313-1450
Mail Stop ISSUE FEE

Sir:

In response to the Notice of Allowance mailed by the United States Patent and Trademark Office on May 4, 2006, enclosed for filing in the above-identified application are the following:

DOCUMENTS ENCLOSED:


☒ Fee(s) Transmittal (PTO form PTOL-85)

☒ The Commissioner is authorized to charge \$1400.00 to Deposit Account No. 15-0665 for the Issue fee associated with this communication.

Respectfully submitted,
Orrick, Herrington & Sutcliffe LLP

Dated: August 2, 2006

By:


Jeffrey A. Miller
Reg. No. 35,287

4 Park Plaza Suite 1600
Irvine, California 92614-2558
(650) 614-7400